

EAST - [Untitled1:1]

FileViewEditToolsWindowHelp

Drafts

BRS:

Pending

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Tagged (0)

UDC

Queue

Trash

L1: (617121) exact or inexact

L2: (16496) (input\$1 or operand\$1 or result\$1 or output\$1) near5 1

L3: (153) round\$3 near10 2

L4: (4) based near 3

L5: (5553) (input\$1 or operand) near10 1

L7: (23101) (intermediate or result\$1 or output\$1) near10 1

L8: (976) 5 near10 7

L9: (16) round\$3 near10 8

L10: (15) 9 not 4

L11: (18) 3 same 8

L12: (4) 11 not 10

L13: (1946) round\$3 with 1

L14: (19) 13 same 8

L16: (1) 14 not 11

L17: (24) 13 same 5 same 7

L19: (5) 17 not (4 or 11 or 14)

Search

Log

Browse

Queue

Clear

DBs

US-PGPUB: USPAT: USOCR: EPO: JPO: DERWENT: IBM_TDB

Default operator

ADJ

Plural

Highlight all hit terms initially

17 not (4 or 11 or 14)

BRS formS&R formImageTextHTML

U	1	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Re	Invent
<input type="checkbox"/>	<input type="checkbox"/>	US 20040078588 A1	20040422	26	Method and apparatus for balanced electronic operations	713/200			Chow, Stanley 1
<input type="checkbox"/>	<input type="checkbox"/>	US 20040030905 A1	20040212	22	Encoding method and system resistant to power analysis	713/189			Chow, Stanley 1
<input type="checkbox"/>	<input type="checkbox"/>	US 20040035032	20040205	23	Method and system for resistance to statistical power analysis	713/189			Chow, Stanley 1

Hits

Details

HTML

NUM